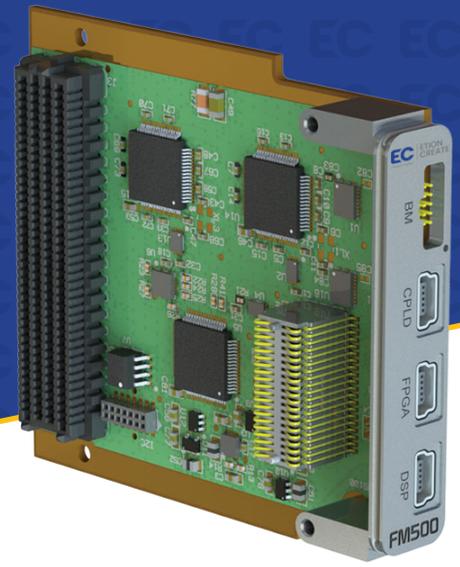


# FM500 V2

Test FMC for VF360 development and maintenance



## SPECIFICATIONS

### Board Management (BM) functionality

- 127 mm 2 x 5 connector
- Connects to Board Management Controller (MANCON) JTAG interface
- Programming and testing of MANCON software

### CPLD functionality

- Mini USB connector
- Connects to Control CPLD JTAG and MANCON serial port on VF 360
- USB Serial Port for MANCON log messages
- USB Blaster II for programming of Control CPLD

### USB Blaster II functionality

- Mini USB connector
- Connects to Stratix® V FPGA
- SignalTap® II Logic Analyser
- FPGA configuration via JTAG
- EPCQ Programming

### XDS 100 functionality

- Mini USB connector
- Connect to TMS320C667X DSP
- In Circuit Emulation (ICE) and DSP debugger
- USB Serial Port for debugging
- Compatible with Code Composer Studio

### Typical Applications

- Monitoring of MANCON messages through serial port
- Programming of Control CPLD with BM USB Blaster II
- Programming of MANCON software through JTAG connector with external ATMEL AVR JTAG ICE3
- FPGA firmware debugging with SignalTap® II Logic analyser
- DSP software debugging and in circuit emulation with XDS 100

### Ordering Information

- Standard order code = FM500 V 2

The **FM500 version 2** is an air cooled Test FMC, typically used during firmware and software development, testing and maintenance cycles of the VF360.

The front panel contains three mini USB connectors and JTAG connector.

The front panel BM header connects to the Board Management (BM) Controller (MANCON) and allows programming and upgrading of the MANCON software with an external Atmel AVR JTAG ICE 3.

The CPLD mini USB connector on the front panel provides a MANCON UART (over USB) and an JTAG (USB Blaster II) interface to program the Control CPLD on the VF360

The FPGA mini USB connector on the front panel is part of a USB Blaster II, implemented on the FMC, that connects to the FPGA via an innovative debug interface connector. It provides SignalTap® II and other JTAG based functionality.

The DSP mini USB connector on the front panel is part of a XDS 100 emulator, implemented on the FMC, that connects to the DSP via the debug interface connector. It provides in circuit emulation (functionality as well as a USB serial port for debugging).

The VF360 EC product is a 3 U VPX carrier card with a Stratix® V FPGA, TMS 320 C 667 X DSP processor and FMC site.



ETION  
CREATE

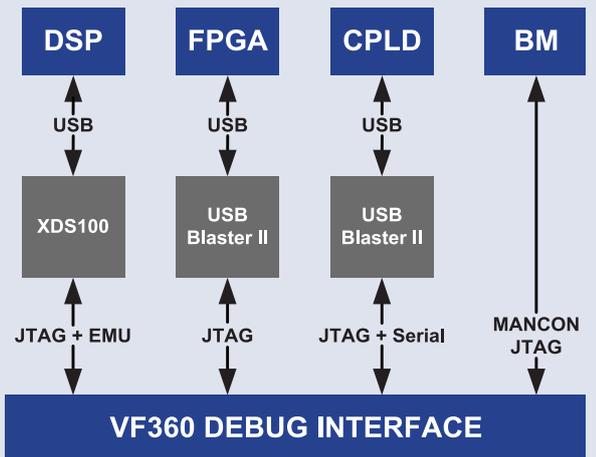
# FM500 V2

Test FMC for VF360 development and maintenance



## Block Diagram

### FRONT PANEL CONNECTORS



HPC

FMC CONNECTOR